# Ceramic SMD Microprocessor Crystal



## **Description**

The ABM7 series of quartz crystals offers fundamental oscillation in the frequency range of 8MHz to 48MHz. Tight frequency tolerance and temperature stability options are available, over extended operating temperature ranges up to -40°C to +105°C. The ABM7 Series is offered in an industry standard 6.0mm x 3.5mm x 1.4mm two pad SMD glass-seal package.



#### **Features**

- Superior heat-resistant glass sealing
- Hermetically seam-sealed ceramic package
- REACH/RoHS II Compliant; Pb in glass, exemption 7(c)
- MSL Level 1

## **Typical Applications**

- Computers, Modems, Communication equipment
- Industrial wide temperature applications

## **Electrical Specifications**

Parameters	Min.	Тур.	Max.	Units	Notes
Frequency Range	8.000		48.000	MHz	Fundamental AT-cut
Operating Temperature Range	-10		+60	°C	See options
Storage Temperature Range	-55		+125	°C	
Frequency Tolerance @ +25°C	-50		+50	ppm	See options
Frequency Stability over the Operating Temperature (ref. to +25°C)	-50		+50	ppm	See options
Equivalent series resistance	See table 1 below		Ω		
Shunt capacitance (C0)			7	pF	
Load capacitance (CL)		18		pF	See options
Drive Level		10	100	μW	
Aging@25°C±3°C			±5	ppm	Frist year
Insulation Resistance	500			МΩ	@ 100Vdc
Sealing Method		Glass Sealing			
Spurious			-3	dB	Fo to Fo+5000 ppm

#### Table 1

Frequency (MHz)	ESR(Ω) max.
8.000-9.999	100
10.000-11.999	80
12.000-15.999	60
16.000-48.000	50

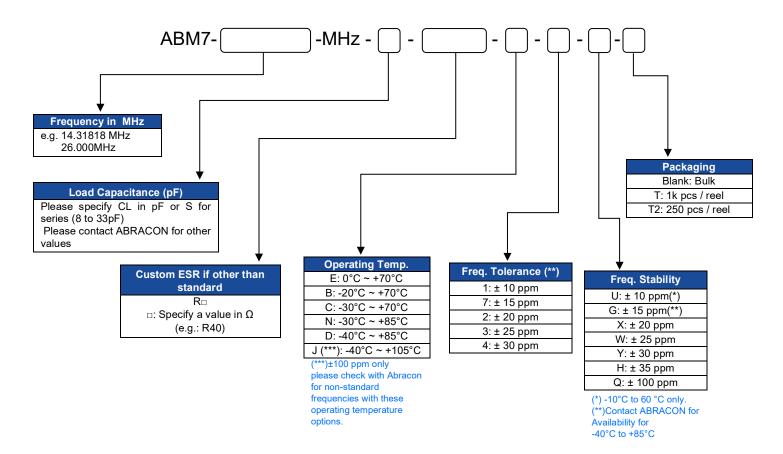
Revision: T Initial Release 4/17/2024

Disclaimer

Check Inventory () Request Samples ()

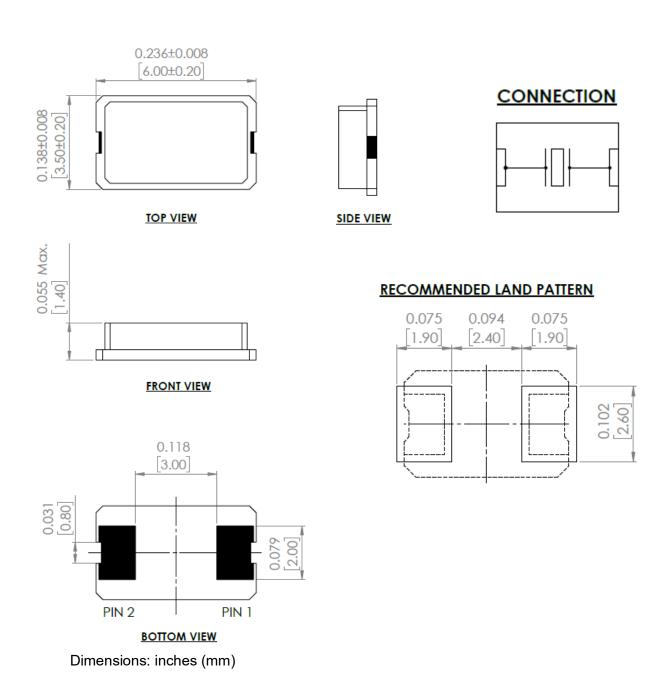


#### **Part Identification**





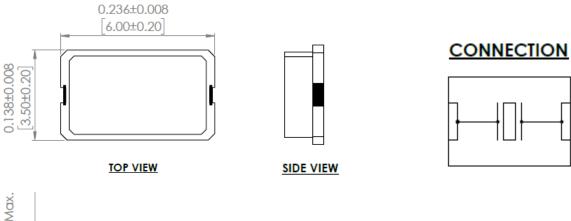
### **Mechanical Dimensions**

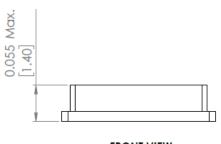


Note: Due to the availability of raw materials, this part may be manufactured with a chamfer on pin 1. Be advised that this does not affect the electrical characteristics of the crystal in any way.

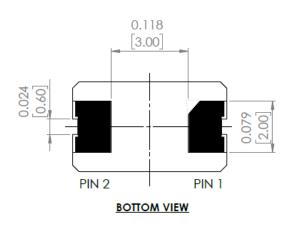


# **Mechanical Dimensions (Alternate)**



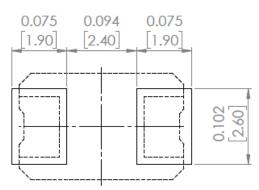


#### **FRONT VIEW**



Dimensions: inches (mm)

# RECOMMENDED LAND PATTERN



Note: Due to the availability of raw materials, this part may be manufactured with a chamfer on pin 1. Be advised that this does not affect the electrical characteristics of the crystal in any way.



# **Reflow Profile [JEDEC J-STD-020]**

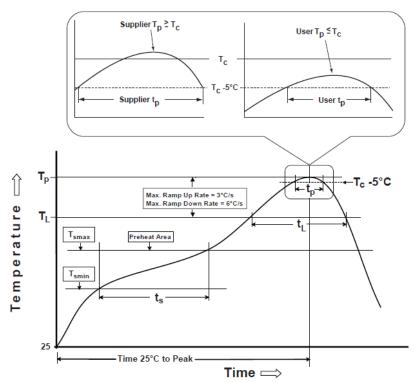


Table 1 **SnPb Eutectic Process** Classification Temperatures (Tc) Package Volume mm<sup>3</sup> Volume mm<sup>3</sup> Thickness <u>></u>350 <2.5 mm 235 °C 220 °C 220 °C 220 °C ≥2.5 mm

Table 2 **Pb-Free Process** Classification Temperatures (Tc) Package Volume mm<sup>3</sup> Volume mm<sup>3</sup> Volume mm<sup>3</sup> Thickness <350 350-2000 >2000 <1.6 mm 260 °C 260 °C 260 °C 1.6 mm - 2.5 mm 260 °C 250 °C 245 °C >2.5 mm 250 °C 245 °C 245 °C

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T <sub>smin</sub> )	100°C	150°C
Temperature maximum (T <sub>smax</sub> )	150°C	200°C
Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60 - 120 sec.	60 - 120 sec.
Average ramp-up rate (T <sub>smax</sub> to T <sub>P</sub> )	3°C/sec. max	3°C/sec. max
Liquidous temperature (T <sub>L</sub> )	183°C	217°C
Time at liquidous (t <sub>L</sub> )	60 - 150 sec.	60 - 150 sec.
Peak package body temperature (T <sub>P</sub> )*	see Table 1	see Table 2
Time $(t_p)^{**}$ within 5°C of the specified classification temperature $(T_c)$	20 sec.	30 sec.
Ramp-down rate (T <sub>p</sub> to T <sub>smax</sub> )	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

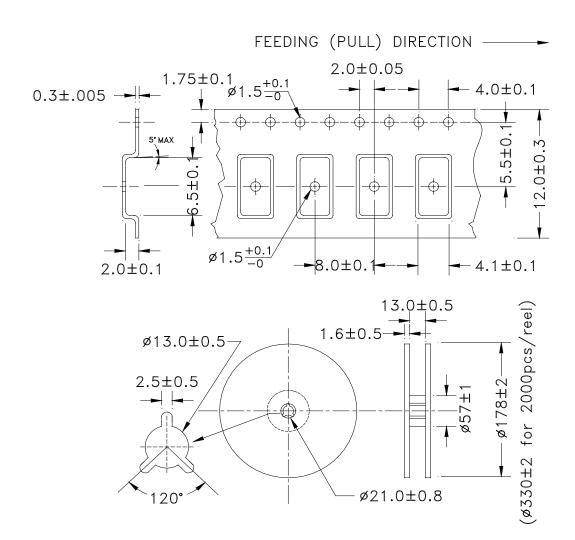
<sup>\*</sup>Tolerance for peak profile temperature (T<sub>P</sub>) is defined as a supplier minimum and a user maximum.

<sup>\*\*</sup>Tolerance for time at peak profile temperature (t<sub>o</sub>) is defined as supplier minimum and a user maximum.



## **Packaging**

T: 1,000pcs/reel T2: 250pcs/reel



Dimensions: inches (mm)