

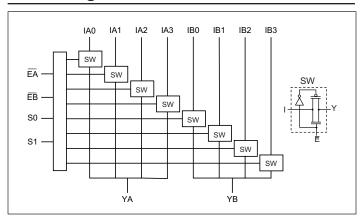


3.3V, Dual 4:1 Mux/DeMux NanoSwitch™

Description

The DIODES PI3B3253 is a 3.3V, Dual 4:1 Multi-plexer/Demulti-plexer with Hi-Z outputs that is pinout compatible with the PI74FCT253T, 74F253, and 74ALS/AS/LS 253. Inputs can be connected to outputs with low On-Resistance (5Ω) with no additional ground bounce noise or propagation delay.

Block Diagram



Truth $Table^{(1)}$

Ena	Enable		Select		W	E 04: 0	
$\overline{\mathbf{E_A}}$	$\overline{\mathrm{E}_{\mathrm{B}}}$	S_1	S ₀	Y _A	YB	Function	
Н	X	X	X	Hi-Z	X	Disable A	
X	Н	X	X	X	Hi-Z	Disable B	
L	L	L	L	_I A ₀	$_{\rm I}{ m B}_{ m 0}$	S1-0=0	
L	L	L	Н	_I A ₁	_I B ₁	S1-0=1	
L	L	Н	L	_I A ₂	_I B ₂	S1-0=2	
L	L	Н	Н	_I A ₃	IB3	S1-0=3	

Note:

1. H = High Voltage Level, L = Low Voltage Level

Features

- Near-Zero Propagation Delay
- 5Ω Switches Connect Inputs to Outputs
- Fast Switching Speed: 5.2ns max.
- Ultra Low Quiescent Power (0.2μA Typical)
 - Ideally Suited for Notebook Applications
- Pin Compatible with 74 Series 253 Logic Devices
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.
 - https://www.diodes.com/quality/product-definitions/
- Packaging (Pb-free & Green):
 - □ 16-pin, QSOP (Q)
 - 16-pin, TSSOP (L)
 - 16-pin, UQFN (ZHD)

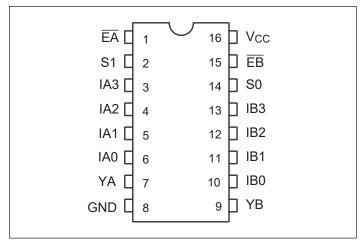
Notes

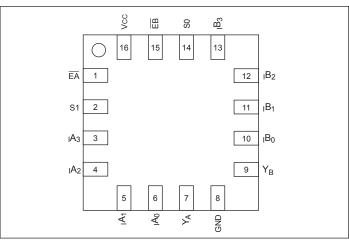
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





Pin Configuration





QSOP, TSSOP Package

UQFN Package

Pin Configuration

Pin#	Pin Name	Description
6, 5, 4, 3 10, 11, 12, 13	IAN, IBN	Data Inputs
14, 2	S ₀₋₁	Select Inputs
1, 15	$\overline{E_A}, \overline{E_B}$	Enable
7,9	Y_A, Y_B	Data Outputs
8	GND	Ground
16	V _{CC}	Power





Maximum Ratings

Above which the useful life may be impaired. For user guidelines, not tested.

Storage Temperature	65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential	0.5V to +4.6V
DC Input Voltage	0.5V to +4.6V
DC Output Current	120mA
Power Dissipation	

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics

Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 3.3\text{V} \pm 10\%$

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			3.7
$V_{\rm IL}$	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I_{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
I_{IL}	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I_{OZH}	High Impedance Output Current	$0 \le I_N, Y_N \le V_{CC}$			±1	
V_{IK}	Clamp Diode Voltage	$V_{CC} = Min.$, $I_{IN} = -18mA$			-1.2	V
D.	Switch On-Resistance ⁽³⁾	V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA or 64mA		5	8	
R _{ON}		$V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA$		10	17	Ω

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25$ °C ambient and maximum loading.
- 3. Measured by the voltage drop between I and Y pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I,Y) pins.

Capacitance

 $T_A = 25$ °C, f = 1 MHz

Parameters ⁽¹⁾	Description	Test Conditions	Тур.	Units
C _{IN}	Input Capacitance		3.0	
C _{OFF}	I _A /I _B Capacitance, Switch Off	$V_{IN} = 0V$	8.0	pF
C _{ON}	I _A /I _B Capacitance, Switch On		36.0	

Notes:

1. This parameter is determined by device characterization but is not production tested.





Power Supply Characteristics

Parameters	Description	Test Condition	$s^{(1)}$	Min.	Typ. ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = Max.$	$V_{IN} = GND \text{ or } V_{CC}$		0.1	3.0	
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	$V_{IN} = 3.0V^{(3)}$			750	μΑ

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, $+25^{\circ}C$ ambient.
- 3. Per TTL driven input (control inputs only); I and Y pins do not contribute to I_{CC}.

Switching Characteristics over Operating Range

D	B 1.11	T O Itul	Com.		TT **	
Parameters	Description	Test Conditions	Min.	Max.	Units	
t _{IY}	Propagation Delay ^(1,2) In to Yn			0.25		
t_{SY}	Bus Select Time, Sn to Yn		1	4.0		
t _{PZH}	Bus Enable Time, \overline{E} to Yn $C_{L} = 50 pF$ $R_{L} = 500 \Omega$		1	3.8	ns	
t _{PHZ}	Bus Disable Time, En to Y	7.L 333-1	1	5.2		

Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.





Applications Information

Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, IN may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail* minimizes power consumption.

Power-Supply Sequencing and Hot Plug Information

Proper power-supply sequencing is recommended for all CMOS devices. Always apply V_{CC} and GND before applying signals to input/output or control pins.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.





Part Marking

Q Package (PdCu)

PI3B 3253QE YWXX

Y: Year W: Workweek

1st X: Assembly Site Code 2nd X: Fab Site Code

Bar above fab code means Cu wire

Q Package-2017 (Au)

PI3B 3253QE YWXX

Y: Year W: Workweek

1st X: Assembly Site Code 2nd X: Fab Site Code

L Package

PI3B 3253LE YYWWXX

YY: Year WW: Workweek 1st X: Assembly Site Code 2nd X: Fab Site Code Bar above fab code means Cu wire

ZHD Package

wEZHDE YWXX

wEZHDE: PI3B3253ZHDE

Y: Year W: Workweek

1st X: Assembly Site Code 2nd X: Fab Site Code

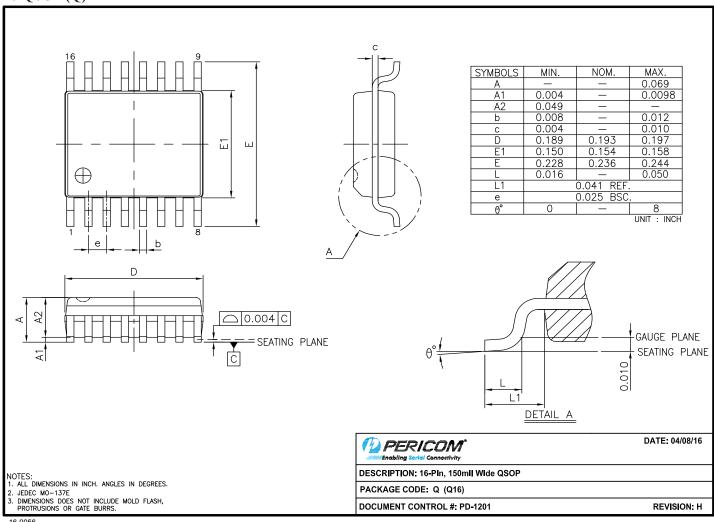
Bar above 2nd "X" means Cu wire





Packaging Mechanical

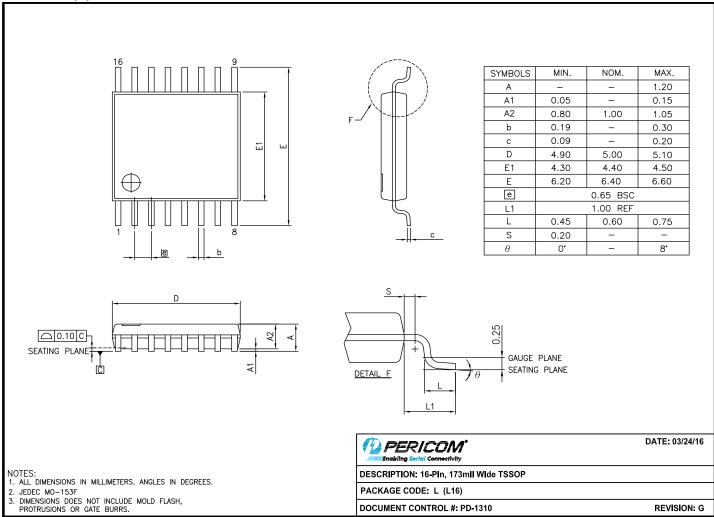
16-QSOP (Q)







16-TSSOP(L)

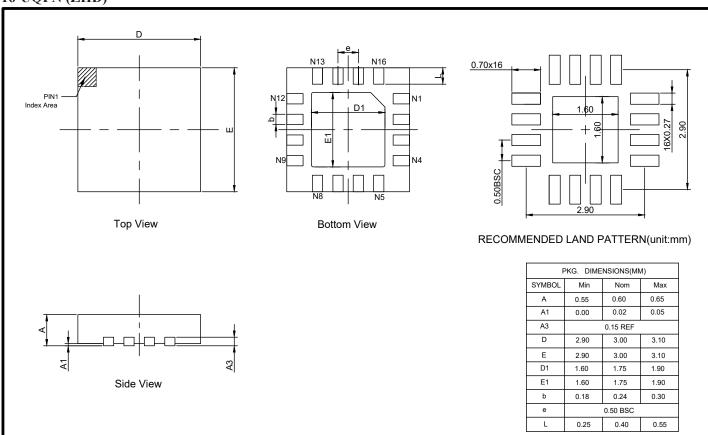


16-0061





16-UQFN (ZHD)



Note:

1. All DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).



For latest package info.

 $please\ check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-packagin$

Ordering Information

Ordering Code	dering Code Package Code Package Desciption	
PI3B3253QEX	Q	16-pin, 150mil Wide (QSOP)
PI3B3253QEX-2017	Q	16-pin, 150mil Wide (QSOP). Au wire bounding.
PI3B3253LEX	L	16-pin, 173mil Wide (TSSOP)
PI3B3253ZHDEX ZHD 16-pin, 3 x		16-pin, 3 x 3, UQFN3030-16 (UQFN)

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





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